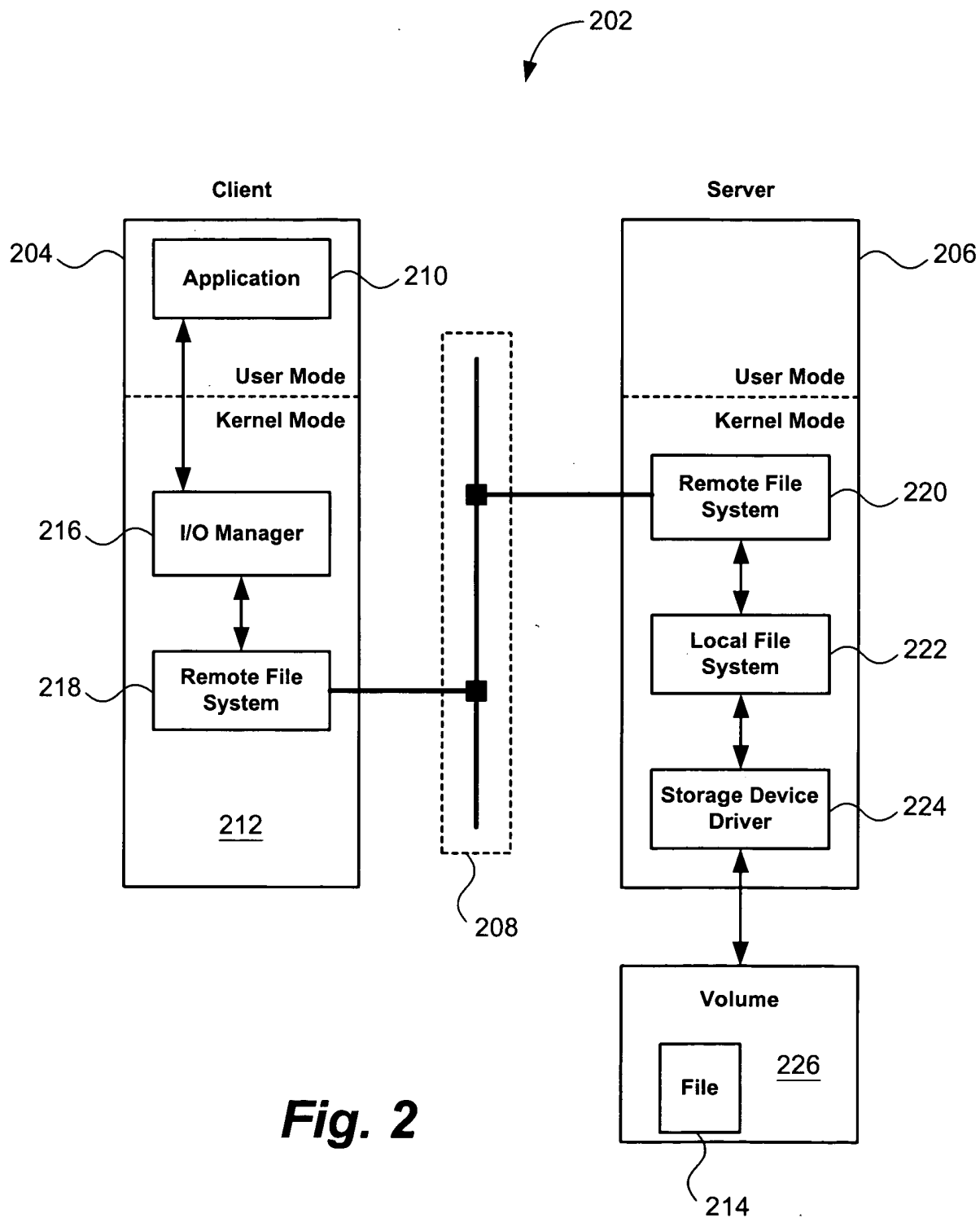


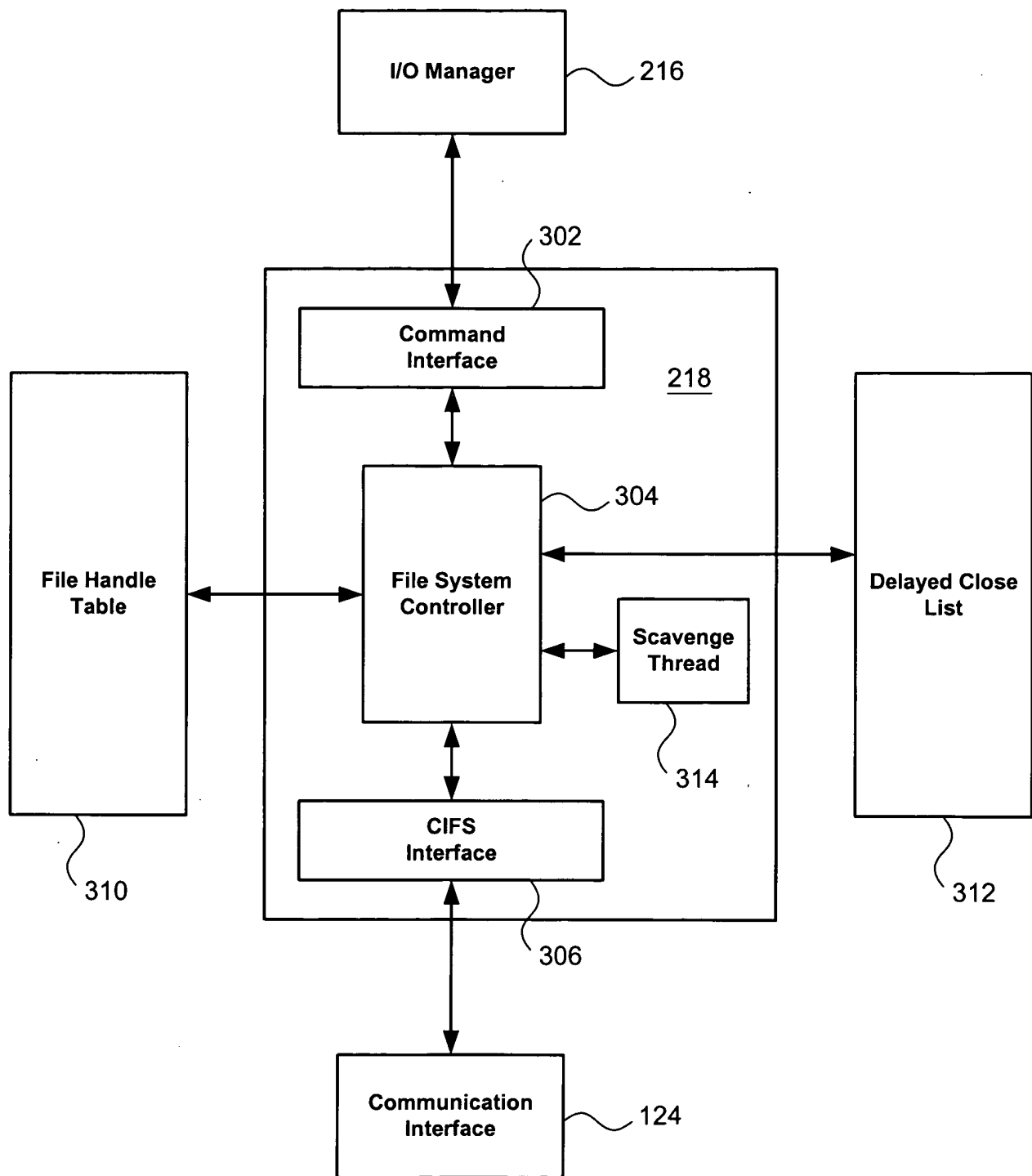


FIG. 2 is a block diagram of a system architecture.

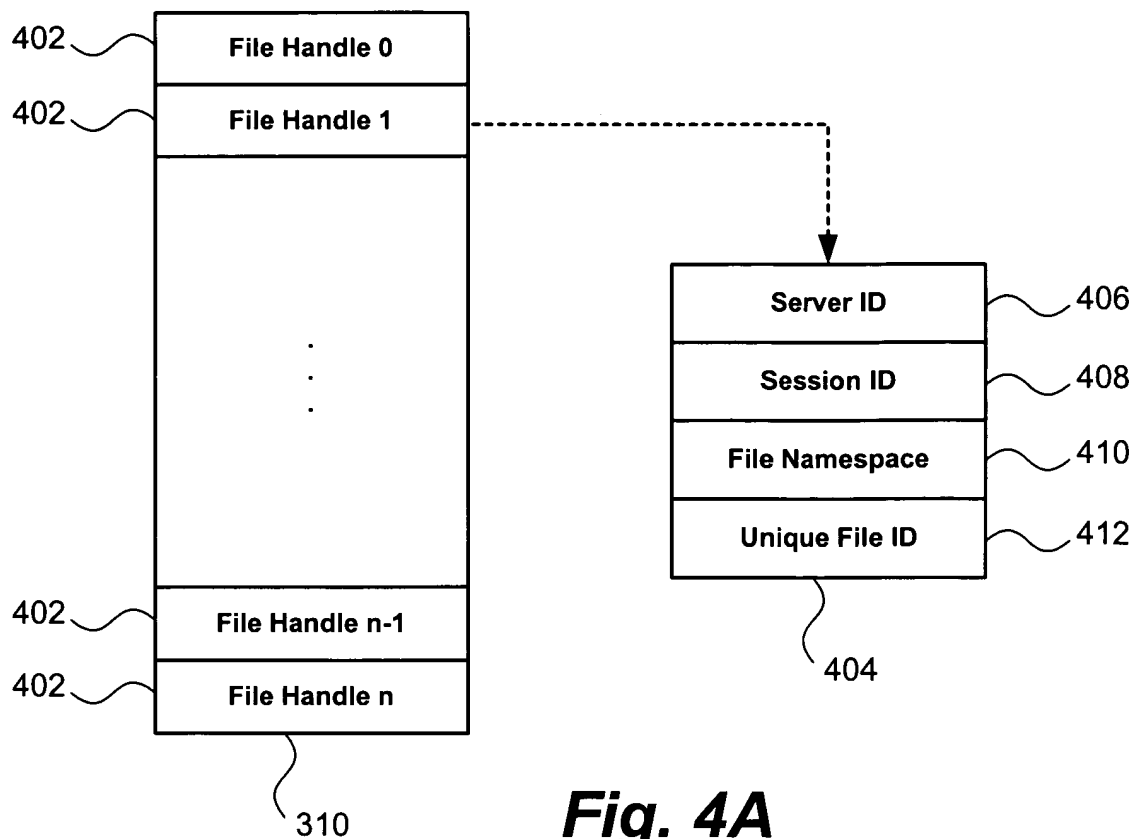


**Fig. 2**

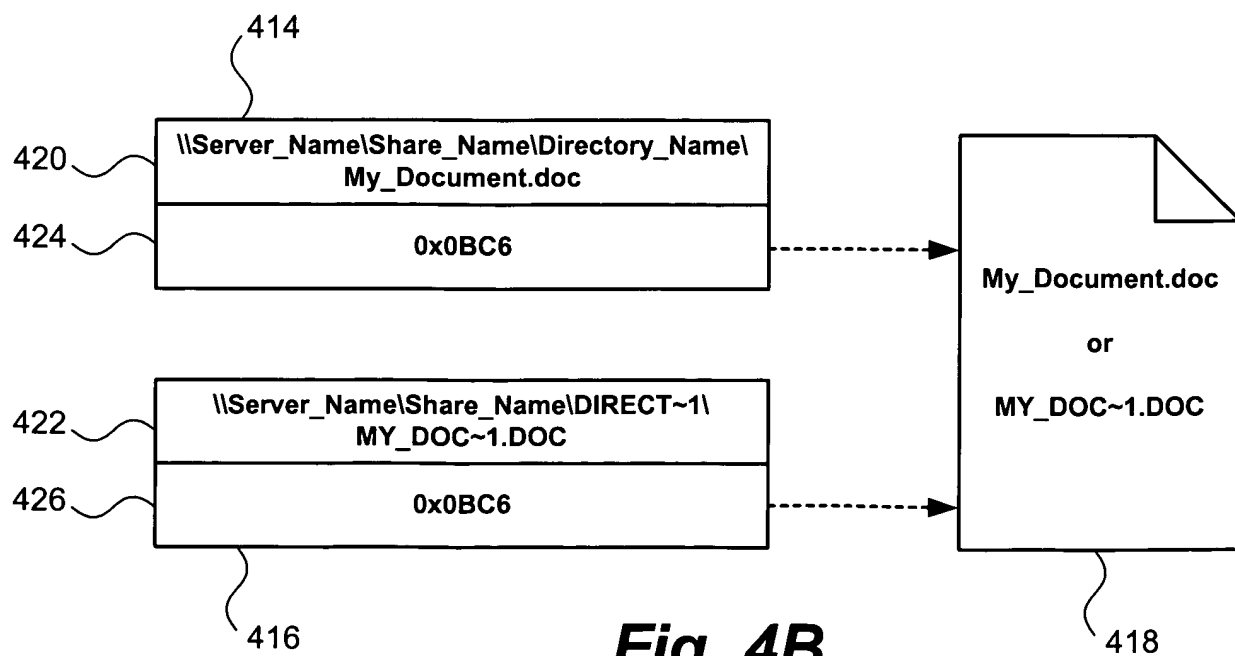
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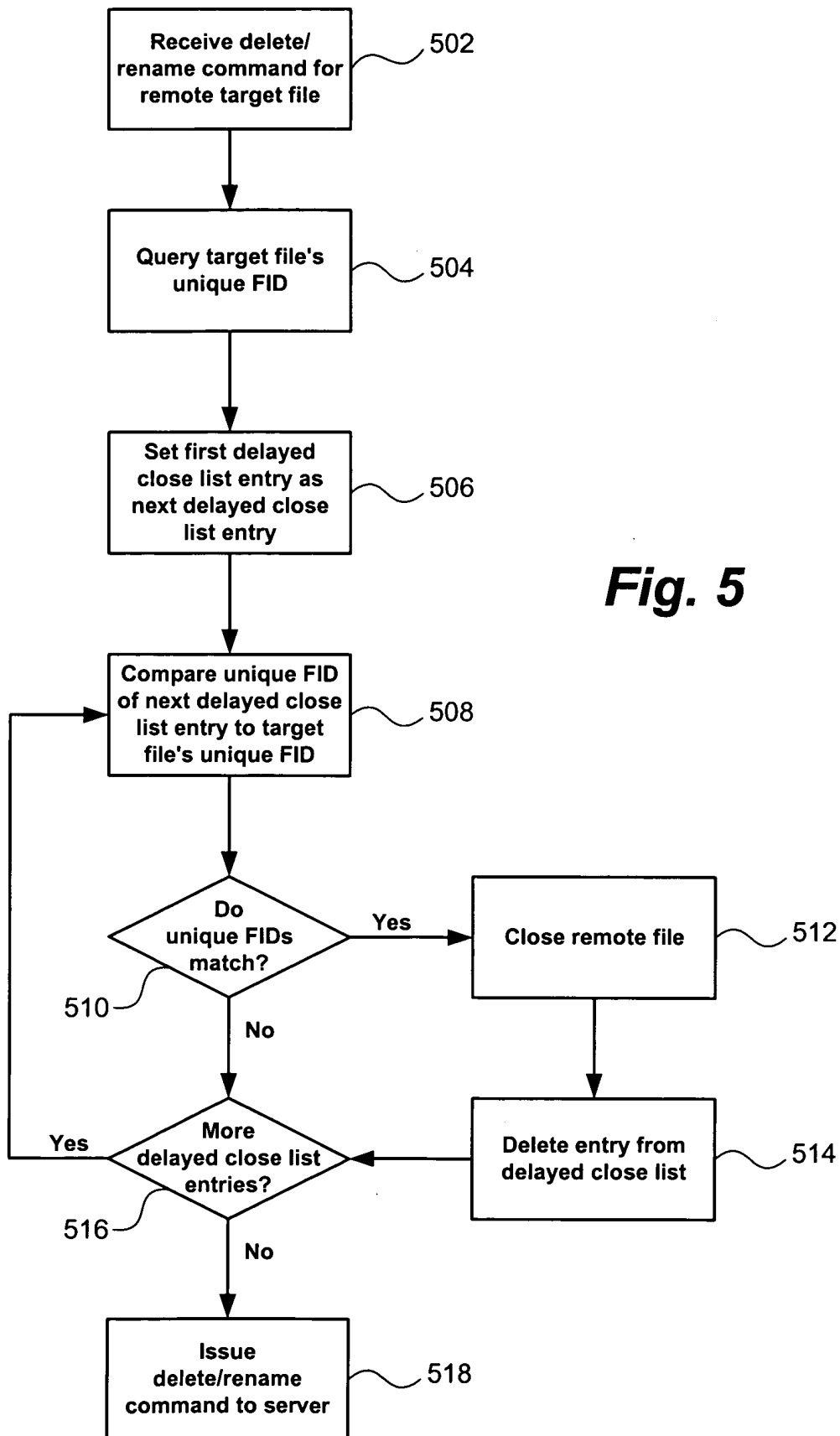
**Fig. 3**



**Fig. 4A**



**Fig. 4B**



**Fig. 5**